1-34.   (Canceled)

35.   (Previously presented) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one control processor operatively connected to said memory, said method comprising the steps of:

receiving, at said receiver, an information transmission including processor instructions and a television program;

performing an error correction routine by a process of checking and correcting said information transmission;

programming said receiver station to perform a failure handling routine in accordance with said processor instructions of said information transmission;

passing information contained in said television program to said memory;

discerning a failure evidencing of at least one of an incomplete and an incorrect television program unit by processing said information passed to said memory corrected in said error correction routine that causes an incompletion operation functionality of the control processor;

executing said failure handling routine in consequence on said step of discerning a failure, said failure handling routine including:

generating and storing operation failure evidencing information of said receiver station,

clearing said memory, and

restoring operation functionality of said at least one control processor based on and in accordance to said stored information, and

instructing said at least one control processor to commence waiting to receive subsequent processor instructions received in said information transmission;

wherein said method controls said receiver station.

36. (Previously presented) The method of claim 35, wherein said failure handling routine further includes placing data at said memory to produce programming in consequence of said step of executing a failure handling routine.

37. (Previously presented) The method of claim 35, wherein said failure handling routine further includes interrupting said at least one processor in accordance with said failure handling routine.

38. (Previously presented) The method of claim 35, wherein said failure handling routine causes said at least one processor to select a code designating an instruction to be executed, and jump to a memory location based on said selected code.

39. (Currently amended) The method of claim 39 38, wherein said code is selected by computing a target number.

40. (Previously presented) The method of claim 35, wherein said step of discerning a failure comprises comparing information stored at a first memory location to information stored at a second memory location.

41. (Previously presented) The method of claim 40, wherein at least one of said first memory location and said second memory location comprises a dedicated register at said at least one processor.

42. (Previously presented) The method of claim 35, wherein said error correction routine includes forward error correction.

43. (Previously presented) The method of claim 35, further comprising the step of: altering said stored operation failure evidencing information to reflect said step of discerning a failure.

44. (Previously presented) The method of claim 36, wherein said produced programming comprises video.

45. (Previously presented) The method of claim 35, wherein said program comprises mass medium programming.

46. (Previously presented) The method of claim 35, wherein said program includes video.

47. (Previously presented) The method of claim 35, wherein said program comprises a computer program.

48.   (Previously presented) The method of claim 35, wherein said step of programming said receiver station comprises:

receiving said failure handling routine from a remote station;

directing said received failure handling routine to a programmable device; and

storing said received failure handling routine at said programmable device.

49.   (Previously presented) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor

operatively connected to said memory, said method comprising the steps of:

receiving, at said receiver station, an information transmission including processor instructions and a mass medium programming including audio programming;

performing an error correction routine by checking and correcting processing of at least a portion of said information transmission;

programming said receiver station to perform a failure handling routine in accordance with said processor instructions of said information transmission;

passing information contained in said mass medium programming to said memory

discerning a failure evidencing at least one of incomplete and an incorrect mass medium programming unit in said memory by processing said information passed to said memory corrected in said error correction routine that causes an incompletion operation functionality of the at least one processor; and

executing a failure handling routine in consequence of said step of discerning a failure, said failure handling routine including:

generating and storing operation failure evidencing information of said receiver station,

clearing said memory, and

restoring operation functionality of said at least one processor based on and in accordance to said stored information, and

instructing said at least one processor to commence waiting to receive subsequent processor instructions received in said information transmission;

wherein said method controls said receiver station.

50.   (Previously presented) The method of controlling a receiver station of claim 49, wherein said step of executing a failure handling routine further includes:

at least one of completing, correcting and discarding at least a portion of said mass medium programming including said audio programming.